

<b>Notice of References Cited</b>	Application/Control No. 10/726,857		Applicant(s)/Patent Under Reexamination KNOWLES, PAUL THOMAS	
	Examiner JUNCHUN WU		Art Unit 2191	Page 1 of 1

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#### NON-PATENT DOCUMENTS

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	V	Rudd "Efficient Exception Handling Techniques for High-Performance Processor Architectures", August 1997, Stanford University.			
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.